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Sheet 1 of 1

Application Number	10/709,569
Filing Date	05/14/2004
First Named Inventor	Anchor Chen
Art Unit	2826
Examiner Name	ABRAHAM, FETSUM
Attorney Docket Number	NAUP0486USA3

[illegible][illegible]

**Examiner
Signature**

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 608. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 501.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Anchor Chen Examiner: ABRAHAM, FETSUM
5 Filing Date: 05/14/2004 Art Unit: 2826
App. No.: 10/709,569 Docket No.: NAUP0486USA3

Title: BIPOLAR JUNCTION TRANSISTOR AND FABRICATING
METHOD

10 To: Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

15 Subject: Information disclosure statement under 37 CFR §1.56

Dear Sir or Madame:

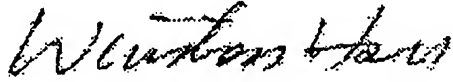
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This is an Information Disclosure Statement in accordance with the duty to disclose information material to patentability under 37 C.F.R. §1.56. The applicant wishes to make of record the document listed on the accompanying form PTO/SB/08. It is respectfully requested that the
25 examiner initials the cited reference on the form and that it be made of record in the application and that a copy of the initialed form be sent to the applicant with the next communication from the examiner.

Since this IDS is filed after the mailing date of the first Office action
30 but before notice of allowance, consideration of the information disclosure statement is hereby requested according to 37 CFR §1.97(e). Each item contained in the IDS was cited in an Office communication from State Intellectual

Property Office of P.R.China on November 5, 2004. The applicant requests that the examiner consider the items contained in this IDS.

5 Respectfully submitted,



Date: 11/30/2004

Winston Hsu, Patent Agent No. 41,526

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